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Pat Slaback	(Depositor's name)
Pat Slaback	(Sign)
April 7, 2005	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/696,226	10/28/2003	Andy T. Nguyen	X-1464-1P-IP US	5260

TITLE OF INVENTION: COUNTER-BASED PHASED CLOCK GENERATOR CIRCUITS AND METHODS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	05/16/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
NGUYEN, MINH T	2816	327-295000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list

- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
(2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1. Lois D. Cartier

2.

3.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

XILINX, INC.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

San Jose, California 95124

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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- ☒ Issue Fee
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5. Change in Entity Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.
☒ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

The Director of the USPTO is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above. NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant, a registered attorney or agent, or the assignee or other part interest as shown by the records of the United States Patent and Trademark Office.

Authorized Signature

Typed or printed name

Keith A. Chanroo

Date April 7, 2005

Registration No. 36,480

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PTOL 85 (Rev 12/04) Approved for use through 04/30/2007

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PAGE 2/2 * RCVD AT 4/7/2005 2:33:14 PM [Eastern Daylight Time] * SVR:USPTO-EFXXF-2/0 * DNIS:7464000 * CSID:408 377 6137 * DURATION (mm:ss):00:56

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COMMISSIONER FOR PATENTS
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Inventor(s): Andy T. Nguyen

Assignee: XILINX, INC.

Serial No.: 10/696,226

conf.no. 5260

Filed: October 28, 2003

Title: Counter-Based Clock Generator Circuits and Methods

Docket No.: X-1464-1P -1P US

Enclosed: Return Receipt Postcard
Fee Transmittal - Part B

Date: April 7, 2005

VIA FACSIMILE 703-746-4000

Atty/Sec: KAC/pbs

[Handwritten signature]